

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/624,716
 Filing Date July 21, 2003
 Inventor Luan C. Tran
 Assignee Micron Technology, Inc.
 Group Art Unit 2811
 Examiner Unknown
 Attorney Docket No. MI22-2357
 Title: Methods of Forming Semiconductor Constructions

OIPE Filing Receipt Corrections
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

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2. Request for Corrected Filing Receipt
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Dated: December 19, 2003

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REQUEST FOR CORRECTION OF FILING RECEIPT

To: OIPE Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450 **Via Facsimile 703-746-9195**

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)
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Sir:

Enclosed is a copy of the Filing Receipt of the original Patent Application referenced above. Applicant respectfully requests correction of the Filing Receipt. The following mistakes need to be corrected:

1. Domestic Priority data as claimed by applicant should read:

This application is a DIV of 10/364,054 02/10/2003 which is a
DIV of 10/229,336 08/26/2002

Applicant respectfully requests the Patent Office records be corrected to accurately reflect the application information as outlined above.

Respectfully submitted,

Dated: December 19, 2003

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711



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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/624,716	07/21/2003	2811	750	MI22-2357	11	9	1

CONFIRMATION NO. 7564

021567

WELLS ST. JOHN P.S.
601 W. FIRST AVENUE, SUITE 1300
SPOKANE, WA 99201

FILING RECEIPT



OC000000011499159

Date Mailed: 12/15/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Luan C. Tran, Meridian, ID;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/364,054 02/10/2003
which is a DIV of ~~10/299,336 11/10/2002~~ 10/229,336 08/26/2002
(*)Data provided by applicant is not consistent with PTO records.

Foreign Applications

If Required, Foreign Filing License Granted: 12/13/2003

Projected Publication Date: 05/20/2004

Non-Publication Request: No

Early Publication Request: No

Title

Methods of forming semiconductor constructions

Preliminary Class

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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